

Date: 20/03/2024



**VIDYA PRATISHTHAN'S KAMALNAYAN BAJAJ INSTITUTE OF
ENGINEERING AND TECHNOLOGY, BARAMATI**

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Minutes of Meeting

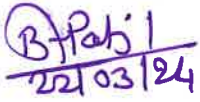

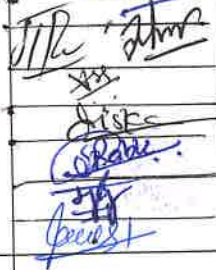
Minutes of Electronics and Telecommunication Engineering meeting held on 20/03/2024
at 03.00 PM (Online Mode in Bharat Forge Lab.)

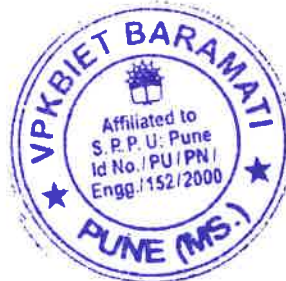
Online meeting link: meet.google.com/tcm-oqcd-ins

Agenda:

1. Previous meetings discussion held and actions taken against on them.
2. Finalization of S.Y. B. Tech (E&TC) syllabus.
3. Finalization of T.Y. and Final year B. Tech structures.

Following BOS members were present in the meeting:-

Sr. No.	Name of Member	Category	Designation within BoS	Name of Institution/ Industry	Signature
1	Dr. B. H. Patil	Head of Department	Chairman	Vidya Pratishthan's Kamalnayan Bajaj Institute of Engineering and Technology, Baramati	
2	Dr. R. K. Shastri	Academic Coordinator of Department	Member Secretary		
3	Dr. S. B. Lande Dr. J. S. Rangole Mr. M. M. Jadhav Mrs. V. S. Surwase Mr. R. S. Piske Ms. S. R. Sable Mr. M. S. Gawade Mrs. M. R. Kate	All Faculty Members	Members		
4	Prof. Sant Sharan Pathak	Subject Expert	Member		Professor and Visiting Faculty Birla Institute of



				Technology, Mesra, Jharkhand	
5	Dr. S. M. Sameer	Subject Expert	Member	Professor, NIT Calicut	Online
6	Dr. Vaibhav Hendre	Subject Expert	Member	Deputy Director, G.H. Rasoni Societys, College of Engineering and Management,Pune.	Online
7	Dr. N. B. Chopade	Subject Expert	Member	Dr Nilkanth Chopade · Deputy Director at Pimpri Chinchwad Education Trust's, Pimpri Chinchwad College of Engineering, Pune	Online
8	Dr. A. V. Deshmukh	Subject Expert	University Nominee	Director, Centre for Industrial Mathematics, Pune	Online
9	Dr. S. U. Kolhar	Postgraduate Meritorious Alumnus	Member	Postgraduate Alumnus, Symbiosis International University Private university in Pune, Maharashtra	Online

In the BOS meeting following points were discussed:

1. Signals and Systems course:
 - i. Need to add discrete signal processing with z transform,
 - ii. Unit 6 can be shifted to Communication Engineering course.
 - iii. MATLAB simulations should be added in the tutorials.
2. Digital signal processing must be made a compulsory course.
3. Digital Electronics and Logic Design in SEM IV. Digital Electronics and Logic Design should be renamed as Digital Logic Design and Control system should be in SEM -III
4. Electronic Devices and Circuits Course: Suggested to utilize **Micro wind simulation tool** in practical course.
5. Suggested to add a mini-project to each course.
6. Python for Circuit Simulation name of the course should be changed as it does not include any circuit simulation. It should be Python Programming and should be added in SEM III and a course on Data Analysis should be in SEM IV.



7. The Embedded System course suggested in MDM is too heavy at second year level. Suggested to change the name of course and contents as well.
8. Entrepreneurship Development course: Specific case studies to be added in unit 6. More focus to be given on learning through activities.
9. No uniformity in the marks and credits.
10. Recent references and textbooks should be added in the Digital logic Design subject.

Link for recorded meeting:

<https://drive.google.com/file/d/1qwSQTk5F0pWruwkC6kQ508PcYw-G7ikR/view>

Glimpses of meeting





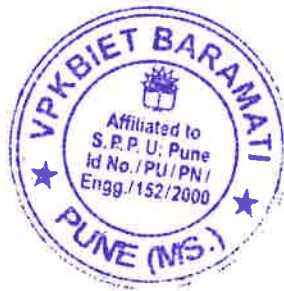
B. H. Patil

Dr. B. H. Patil

BOS Chairman and
Head Dept. of E&TC
VPKBIET, Baramati

Head

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VPKBIET, Baramati-413133



R. S. Bichkar

Dr. R. S. Bichkar

Principal/ Director
VPKBIET, Baramati
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